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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Vickram R. Vathulya

Title of Invention : CIRCUIT AND METHOD FOR INPUT SIDE
IMPEDANCE MATCHING OF A POWER
AMPLIFIER IN AN ELECTRONIC DEVICE

Date Filed : March 19, 2001

Serial No. : 09/811,647

Examiner : BETTENDORF, JUSTIN P

Art Unit : 2817

Attorney Docket No. : US 010082

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Washington, DC 20231

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AMENDMENT

SIR:

This is responsive to the Office Action dated July 18, 2002, in connection with the
above-referenced patent application.

The applicant has amended the application as follows:

IN THE SPECIFICATION:

On page 7, please amend the paragraph at lines 3 -11 as follows:

In most cases, impedance at the input side of a power amplifier can be modeled as a resistor

56 having a value R_{68} in series with a capacitor 58 having a capacitance $-jX_{c70}$. As will be
described in further detail below, impedance transformer network 50 matches the input side